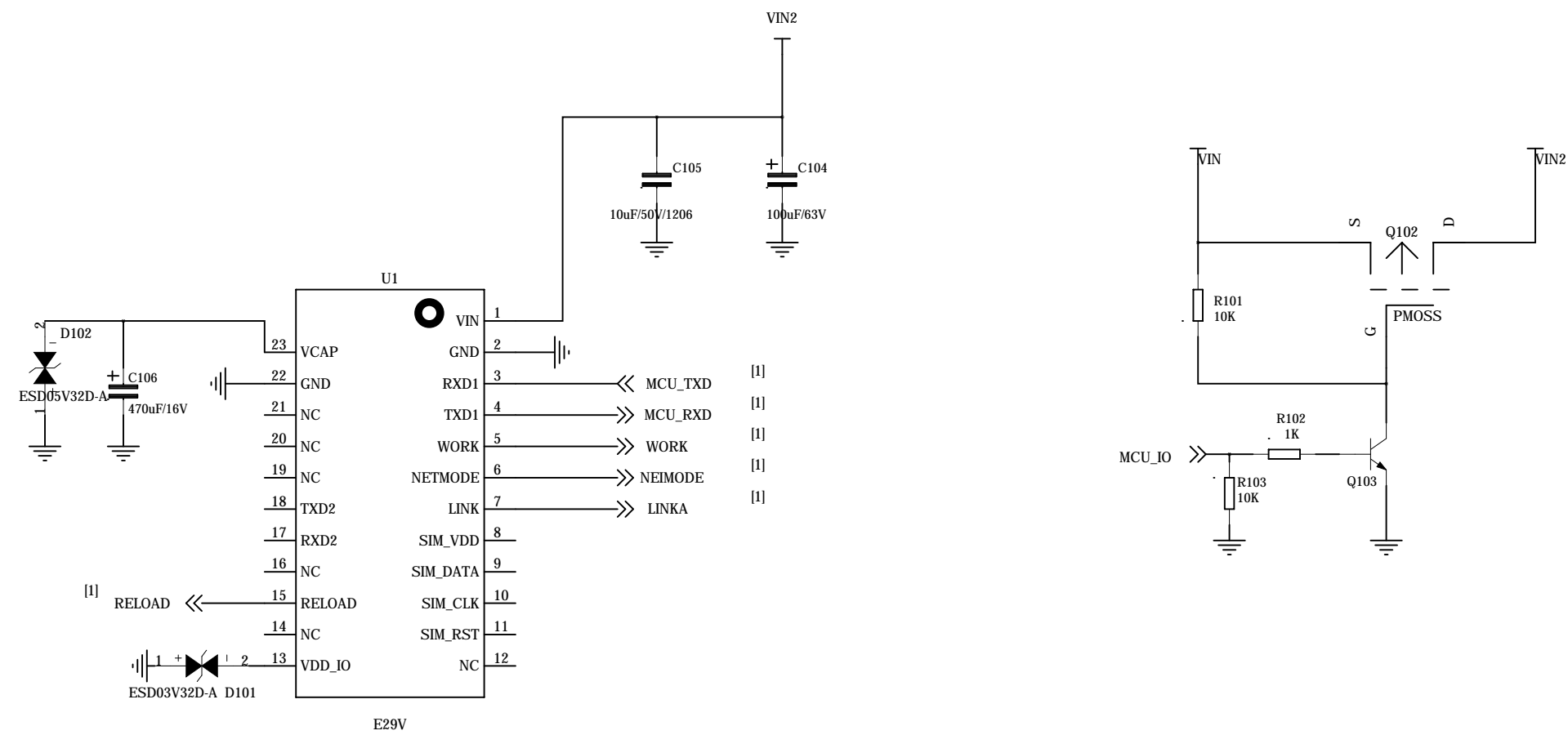


1.POWER A

VIN:DC 5~36V



1.POWER B

VIN:DC 5~36V

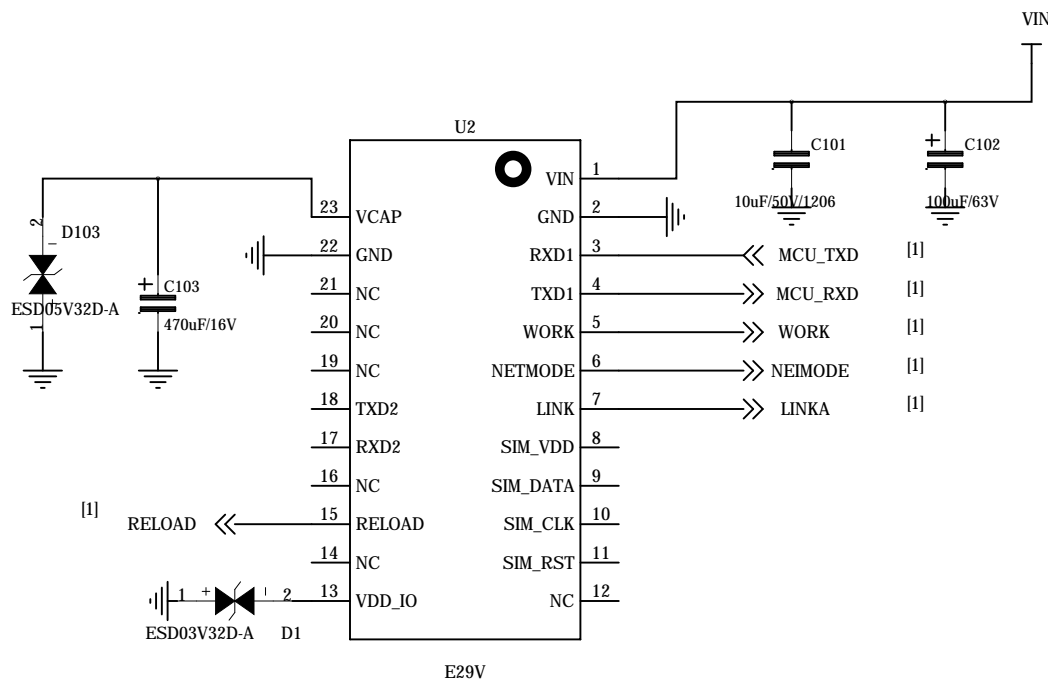
- DESIGN NOTES:
- 1 reload 10K inside the module has been pulled up to VDD_EXT

2 SIM circuit is built in and can be pulled out directly if needed

3 IO design needs to pay attention to level conversion

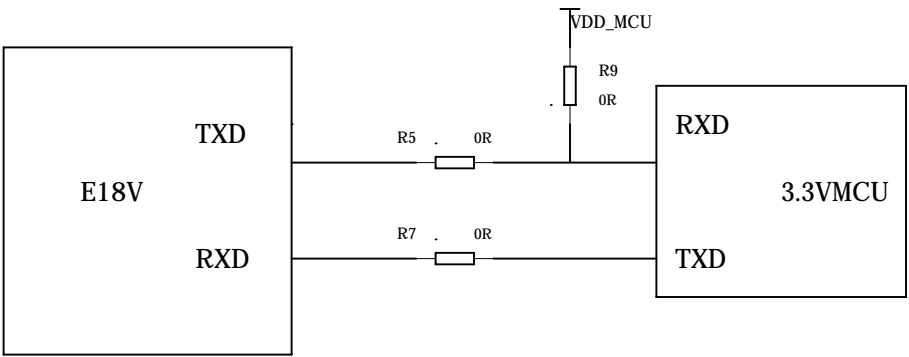
4 VCAP,VDD_IO ESD is necessary

5 POWER A is recommended

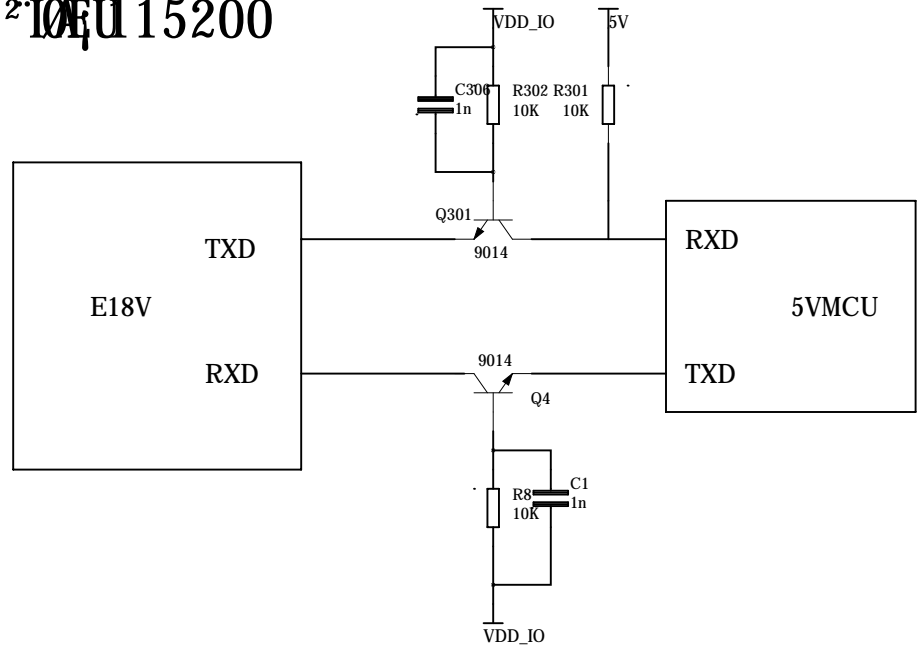


3.UART

If MCU:3.3V



If MCU:5V



4.IO DESIGN

